1. Immediate addressing

op	rs	rt	Immediate

2. Register addressing

op	rs	rt	rd	. . .	funct

Registers

Register

3. Base addressing

op	rs	rt	Address

Register

$+$

Memory

Byte	Halfword	Word
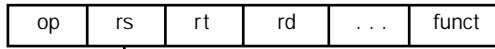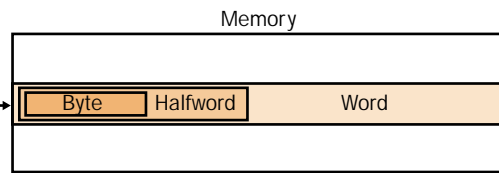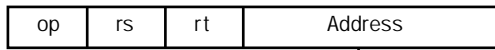
4. PC-relative addressing

op	rs	rt	Address

PC

$+$

Memory

Word
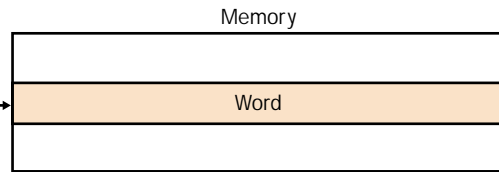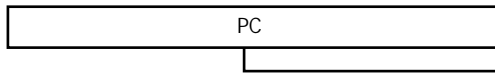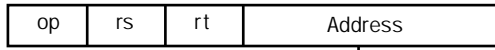
5. Pseudodirect addressing

op	Address

PC

Memory

Word